

IN THE U.S. PATENT & TRADEMARK OFFICE

By electronic communication

<u>First Named Inventor: Thomas J Heller JR :</u>	<u>GROUP ART UNIT 2187</u>
<u>Pat Appln: Serial No. 10/603251:</u>	<u>Examiner: Kalpit Parikh</u>
<u>Filed: June 25, 2003 :</u>	<u>Date: June 11, 2008</u>
<u>Title: Multiprocessor System with Dynamic Cache Coherency Regions :</u>	
<u>Attorney Docket POU920020123US1 :</u>	<u>Confirmation Number: 3683</u>
<u>Deposit Account: 09-0463 :</u>	Lynn L. Augspurger, Attorney, Reg No. 24,227 :
Mailing Address:	IBM Intellectual Property Law
	2455 South Road, P386
	Poughkeepsie, NY 12601

CORRECTIVE AMENDMENT (Responsive to OA Dated 11/26/2007)
AFTER Non-Compliant Notice of 6/05/2008
EXPEDITED PROCESSING REQUESTED

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

A. Introductory Comments:

In reply to the Non-Compliant Official action mailed 6/05/2008 Applicants respectfully request entry of the following corrective amendment to place the application in condition for allowance in light of the remarks and these amendments. (The Commissioner is also hereby authorized to charge payment of the fees associated with this communication or credit any overpayment to Deposit Account No. 09-0463. Those expected are already part of the separate fee authorization.)